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#### **EUROPEAN PATENT APPLICATION**

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71 Applicant: ALCATEL N.V.
Strawinskylaan 537 (World Trade Center)
NL-1077 XX Amsterdam(NL)

Inventor: Powell, William Edward 201 Trotters Ridge Drive Raleigh NC 27614(US)

Inventor: Weeber, William Bernard

7917 Blaney Franks Road Apex NC 27502(US)

Inventor: Roger, George André Charles Pavillon Marguerite, Parc de Lormoy F-91240 Saint Michel Sur Orge(FR)

Representative: Weinmiller, Jürgen et al Lennéstrasse 9 Postfach 24 W-8133 Feldafing(DE)

- Parallel pseudo-random generator for emulating a serial pseudo-random generator and method for carrying out same.
- © A parallel pseudo-random generator for emulating a serial pseudo-random generator that generates serial outputs such that the next serial output value is based upon an Exclusive OR combination of at least two preceding serial output values the maximum preceding serial output value defined as the Pth preceding serial output value, where P is an integer greater than one; comprising:

A) at least P latches, each latch having an output having a logic value 1 or 0 and an input operable upon receipt of a clock signal, for receipt of data for controlling the next logic value on the latch output;

B) at least P Exclusive OR gates, each having at least two inputs and one output, each Exclusive OR gate output connected to a corresponding input of one latch so as to define the next value of the latch output upon receipt of the next clock signal; and

C) means for connecting each input of each Exclusive OR gate to one latch output so that the output of each Exclusive OR gate represents the

corresponding next value of the latch to which This Exclusive Or gate output is connected.

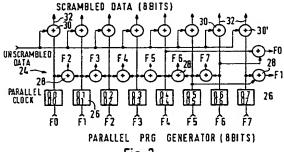


Fig. 2





### **EUROPEAN SEARCH REPORT**

Application Number

EP 90 10 8555

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Category	of relevant pa		to claim	APPLICATION (Int. Cl.5)
X	ELEKTRONIK, vol. 32 1983, pages 67-70; "Parallel arbeitend Descrambler und Zufallsfolgen-Gener * Page 68, paragrap paragraph 4; figure	T. HERMES et al.: e Scrambler, atoren" h 3; page 69,	1-4	H 04 L 25/49
T	THE RADIO AND ELECT 45, no. 4, April 19 J.J. O'REILLY: "Ser generation of m-seq * Whole document *	ies-parallel	1-4	
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